



FORM PTO-1449 FOURTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT		ATTY. DOCKET NO. 1744.0450003	APPLICATION NO. 09/525,615
		INVENTORS Sorrells et al.	
		FILING DATE March 14, 2000	ART UNIT 2631

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
PF	AA57	6,335,656 B1	01/2002	Goldfarb et al.			
	AB57	6,690,232 B2	02/2004	Ueno et al.			
	AC57	5,636,140	06/1997	Lee et al.			
	AD57	6,366,622 B1	04/2002	Brown et al.			
	AE57	6,600,795 B1	07/2003	Ohta et al.			
	AF57	4,250,458	02/1981	Richmond et al.			
	AG57	5,760,629	06/1998	Urabe et al.			
	AH57	6,084,465	07/2000	Dasgupta			
↓	AI57	6,204,789 B1	03/2001	Nagata			

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ						Yes No
	AK						Yes No
	AL						Yes No
	AM						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

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AA						
AB						
AC						
AD						
PF	AE56 4,132,952	01/1979	Hongu et al.			
	AF56 4,441,080	04/1984	Saari			
	AG56 4,873,492	10/1989	Myer			
	AH56 5,697,074	12/1997	Makikallio et al.			
↓	AI56 5,784,689	07/1998	Kobayashi			

FOREIGN PATENT DOCUMENTS

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AJ						Yes No
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AL						Yes No
AM						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

AN			
AO			
PF	AP 59	Deboo, Gordon J., <i>Integrated Circuits and Semiconductor Devices</i> , 2 nd Edition, McGraw-Hill, Inc., pp. 41-45 (1977).	
AQ			
AR			

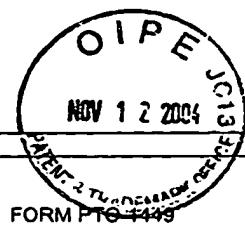
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Page 3 of 3

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EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
MP	AA58	6,064,054	05/2000	Waczynski et al.		
	AB58	5,218,562	06/1993	Basehore et al.		
	AC58	5,239,496	08/1993	Vancraeynest		
	AD58	5,896,304	04/1999	Tiemann et al.		
	AE58	6,005,903	12/1999	Mendelovicz		
↓	AF58	5,834,979	11/1998	Yatsuka		
AG						
AH						
AI						
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